



PATENT

Case Docket No. ADTST.031AUS

Date: November 11, 2005

Page 1

In re application of: Hiroaki Yamoto et al.

App. No. : 09/941,396

Filed : August 28, 2001

For : METHOD FOR DESIGN VALIDATION OF
COMPLEX IC

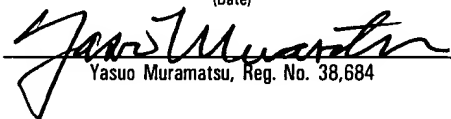
Examiner : Vuthe Siek

Art Unit : 2815

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

November 11, 2005

(Date)


Yasuo Muramatsu, Reg. No. 38,684

TRANSMITTAL

ASSISTANT COMMISSIONER FOR PATENTS
ALEXANDRIA, VA 22313-1450

Sir:

Transmitted herewith is a response to the office action in the above-identified utility application.

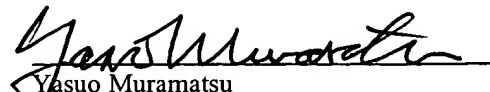
() An extension of time to respond for * month(s) is hereby requested.

Time Extension Fee:

- ☐ one month (\$120 large entity)
- ☐ two months (\$450 large entity)
- ☐ three months (\$1020 large entity)

Enclosed are:

- (X) Amendment in fifteen (15) pages.
- (X) Terminal disclaimer in two (2) pages.
- (X) Check of \$130 for terminal disclaimer.
- (X) Change of correspondence address.
- (X) Return prepaid postcard.


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